



PATENT

Customer No. 22,852  
Attorney Docket No. 04329.2613

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Katsuhiko HIEDA ) Group Art Unit: T. Le  
Application No.: 09/916,509 ) Examiner: 2814  
Filed: July 30, 2001 )  
For: FIELD EFFECT TRANSISTOR )  
USING SIDE-WALL GATES TO )  
PREVENT PUNCH-THROUGH )  
(As Amended Herein) )

Commissioner for Patents  
Washington, DC 20231

Sir:

**AMENDMENT**

In reply to the Office Action dated May 17, 2002, the period for response to which extends through August 19, 2002 (with August 17, 2002 being a Saturday), please amend the application as follows:

**IN THE TITLE:**

Please change the title to read -FIELD EFFECT TRANSISTOR USING SIDE-WALL GATES TO PREVENT PUNCH-THROUGH--.

B1

**IN THE SPECIFICATION:**

Please amend the specification as follows:

#8B And.  
M. Brunson  
8/28/02  
RECEIVED  
MIS 21 2002  
TECHNOLOGY CENTER 2800

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